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5/31/02

566.38877X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: TAMURA et al.  
Serial No.: 09/635,217  
Filed: August 10, 2000  
For: SEMICONDUCTOR MEMORY  
Group: 2186  
Examiner: A. Patel

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MAY 31 2002  
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SUPPLEMENTAL RESPONSE

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

May 29, 2002

Sir:

In supplement to the Response to Restriction Requirement filed May 3, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

Please add the following new claims to the application:

--6. An external storage device according to claim 1,  
wherein the non-volatile semiconductor memory includes  
plural areas in which a data area is made of a protected data area  
and a non-protected data area, and  
wherein, when the host computer is to access the  
protected data area, the external storage device performs  
authentication through an authentication procedure for accessing to  
the protected data area.